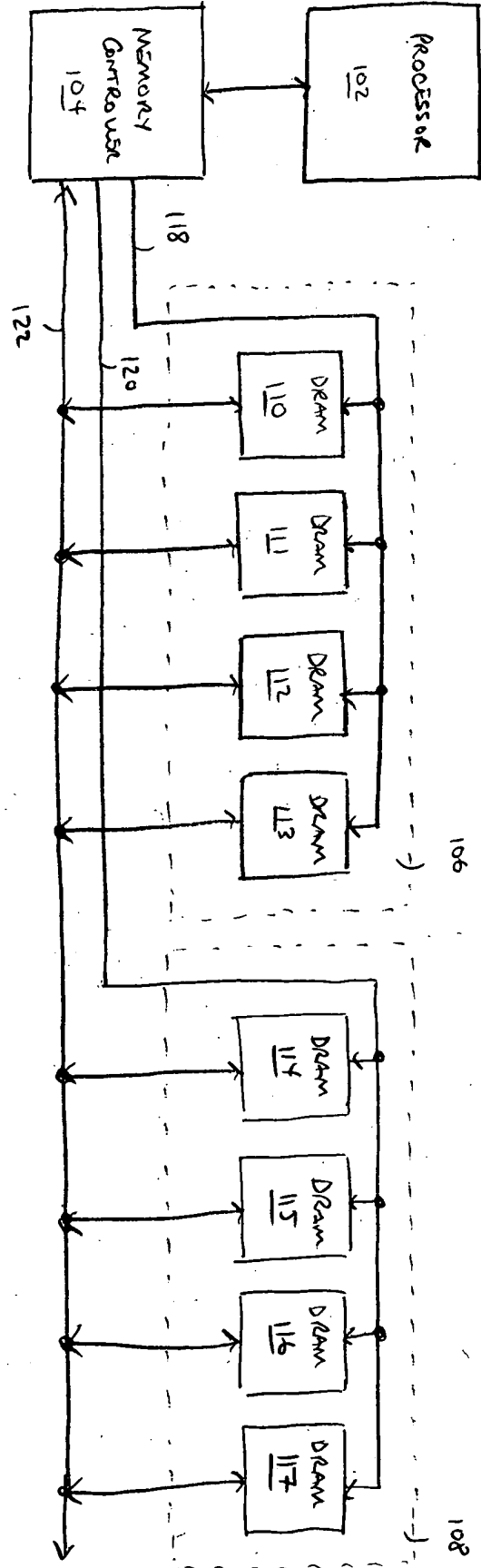




365T20" 4E22E2060

FIG. 1 (PRIOR ART)



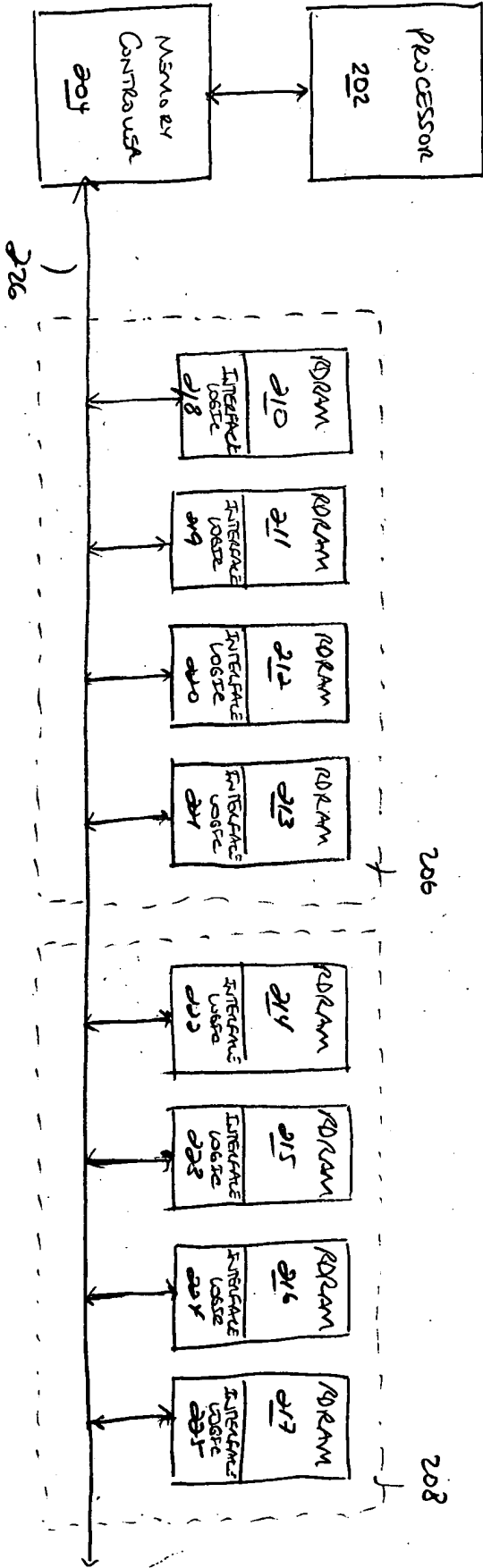


FIG. 2 (Prior Art)









- 500

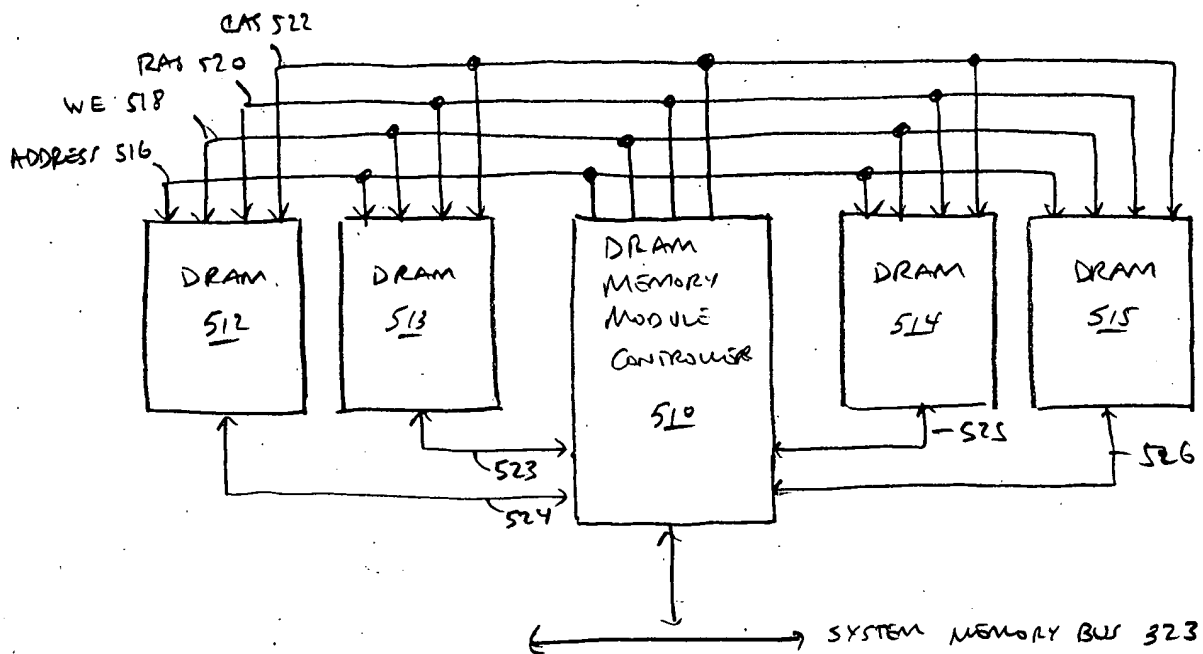


FIG. 5

- 600

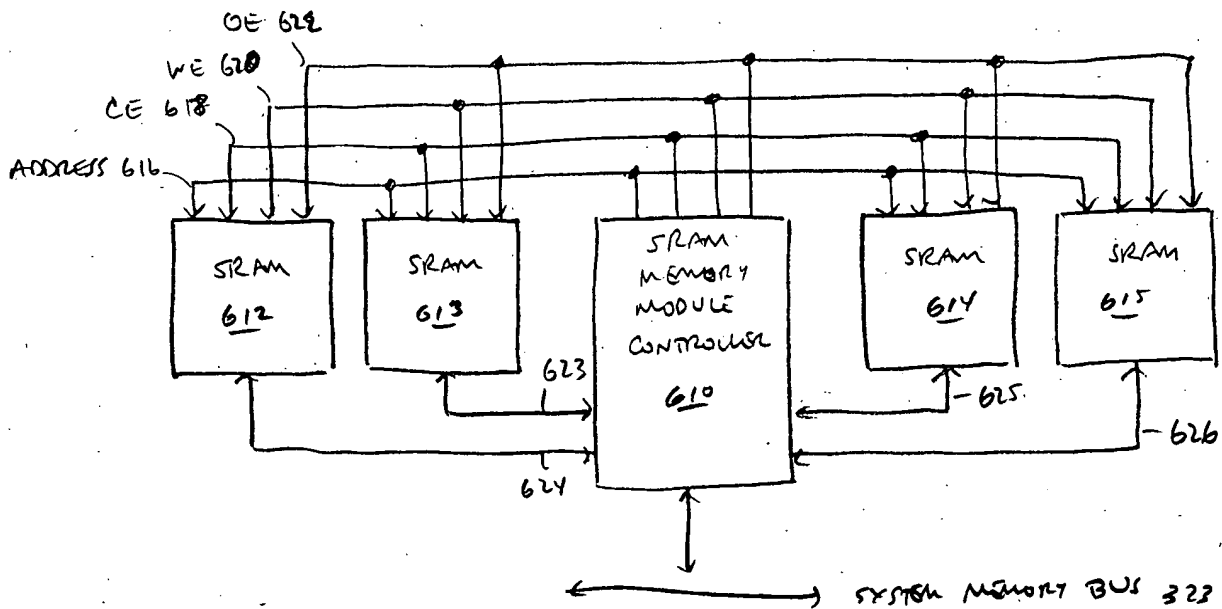


FIG. 6



700

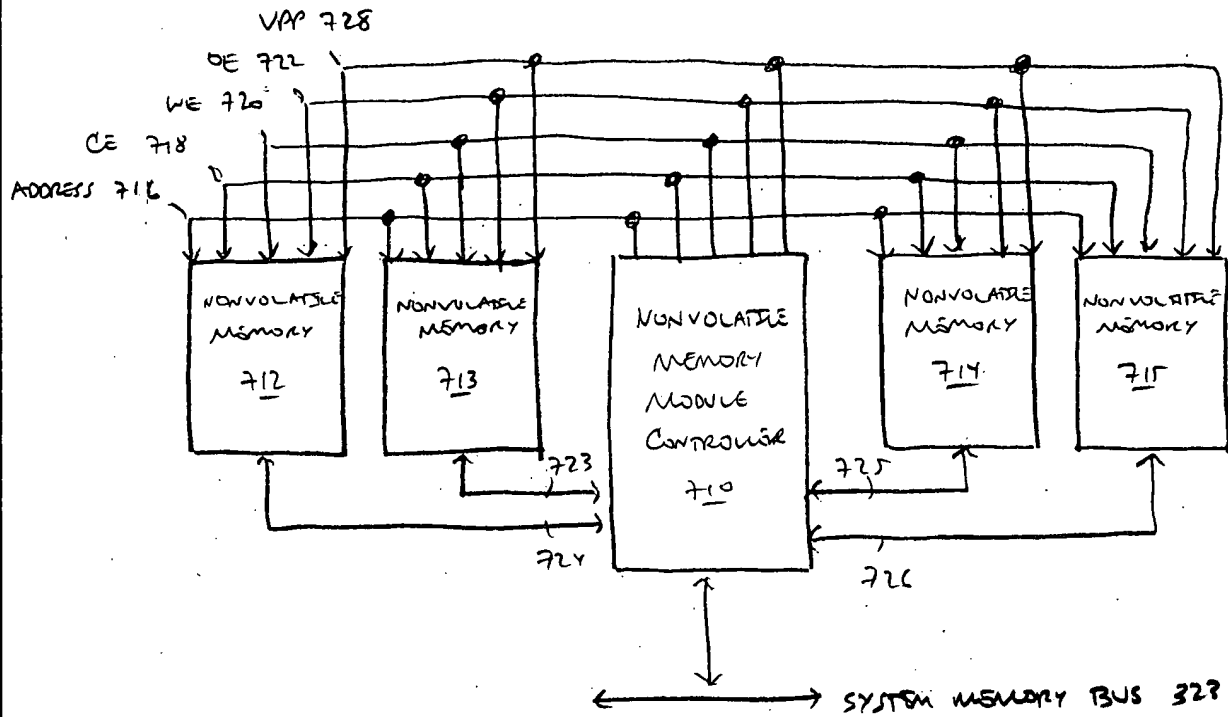


FIG. 7



2020 RELEASE

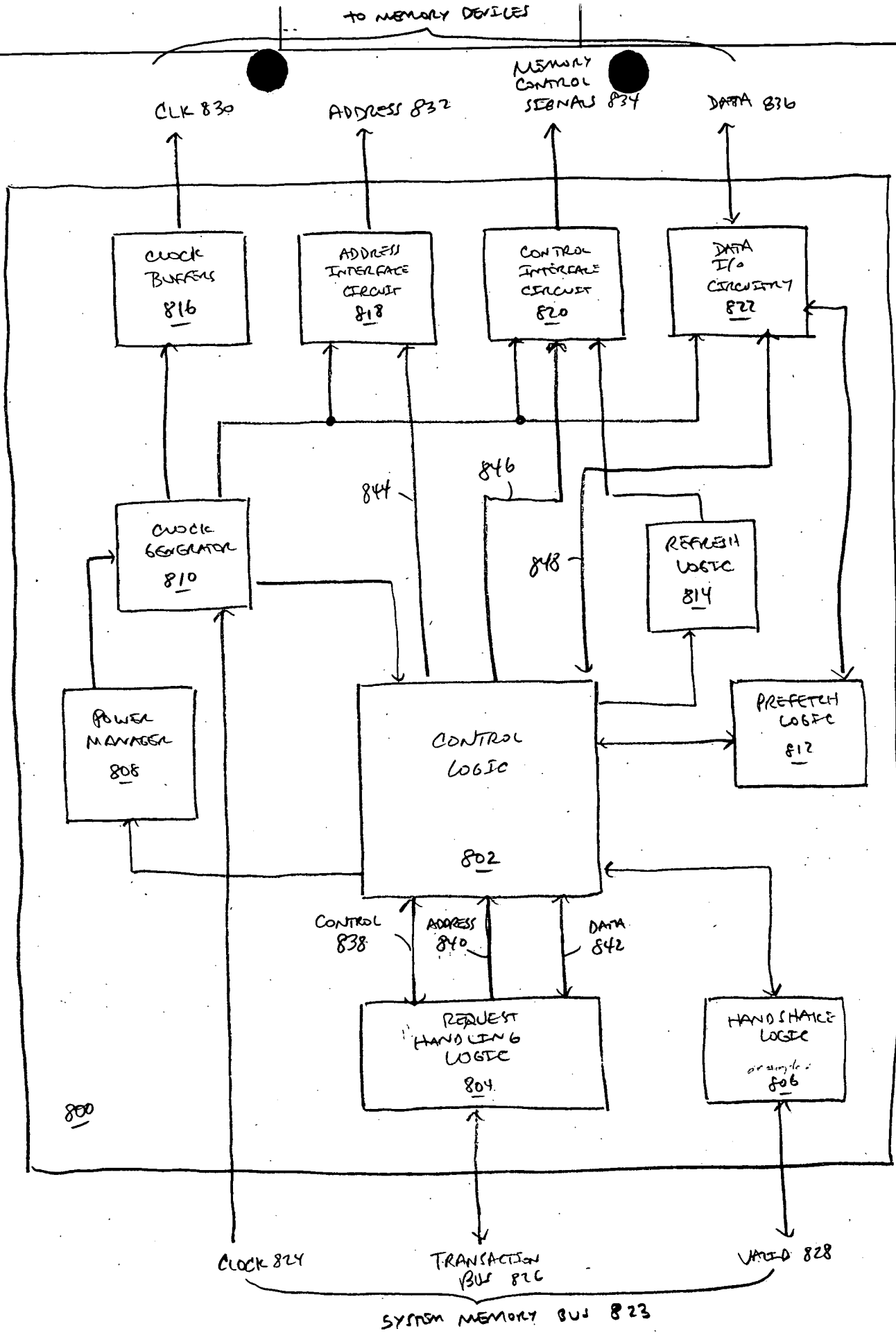


FIG. 8

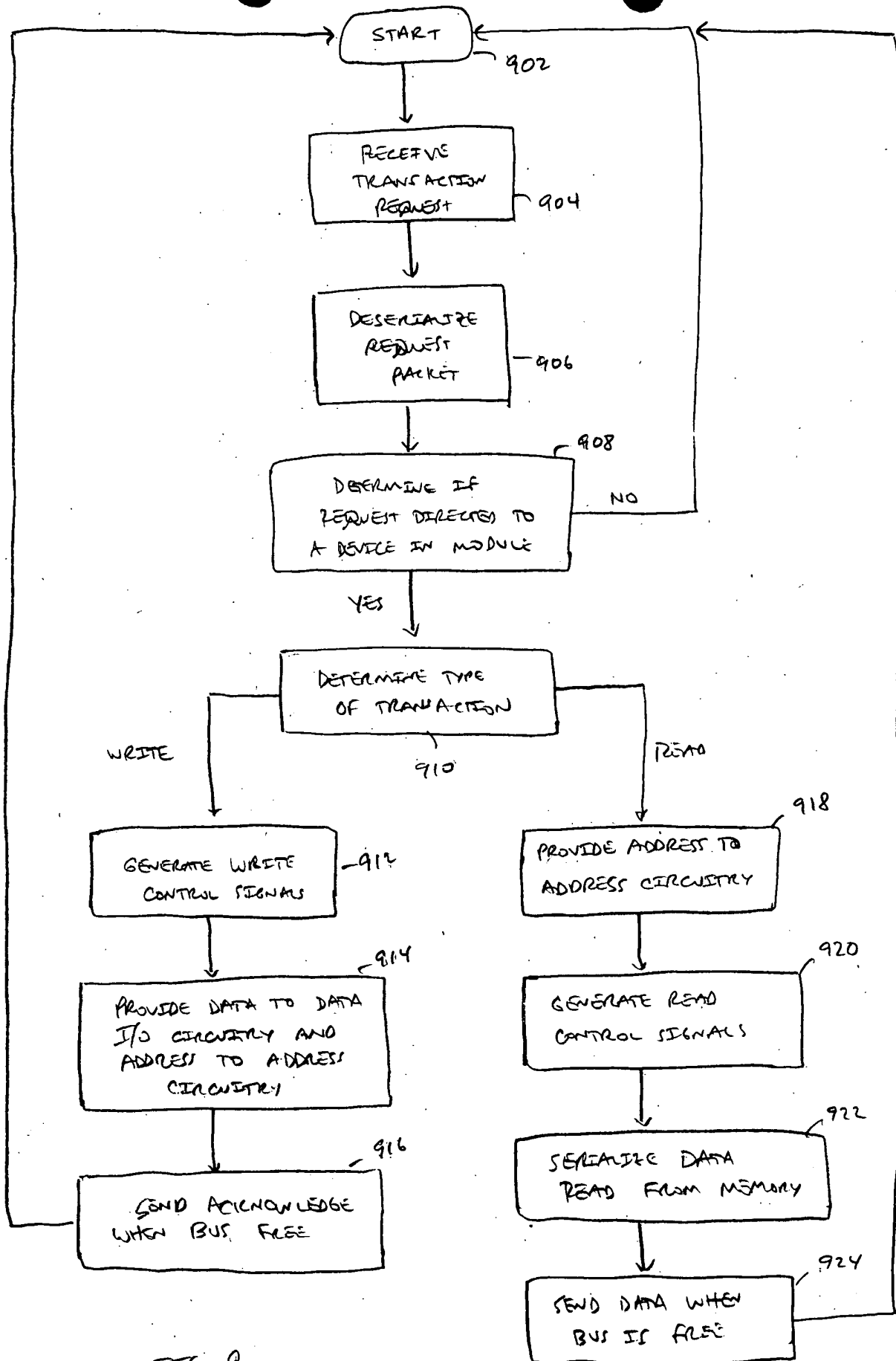
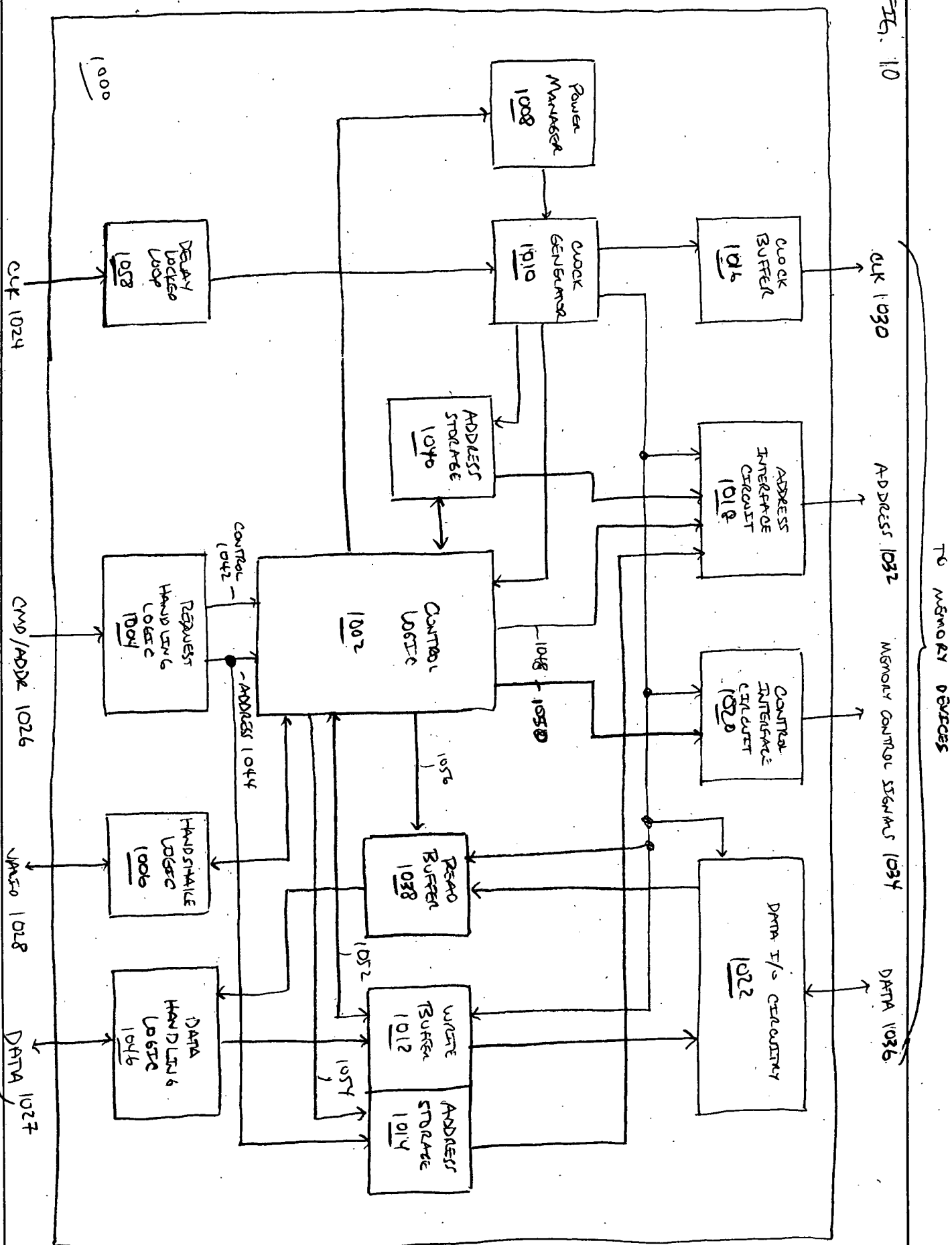


FIG. 9

FIG. 10



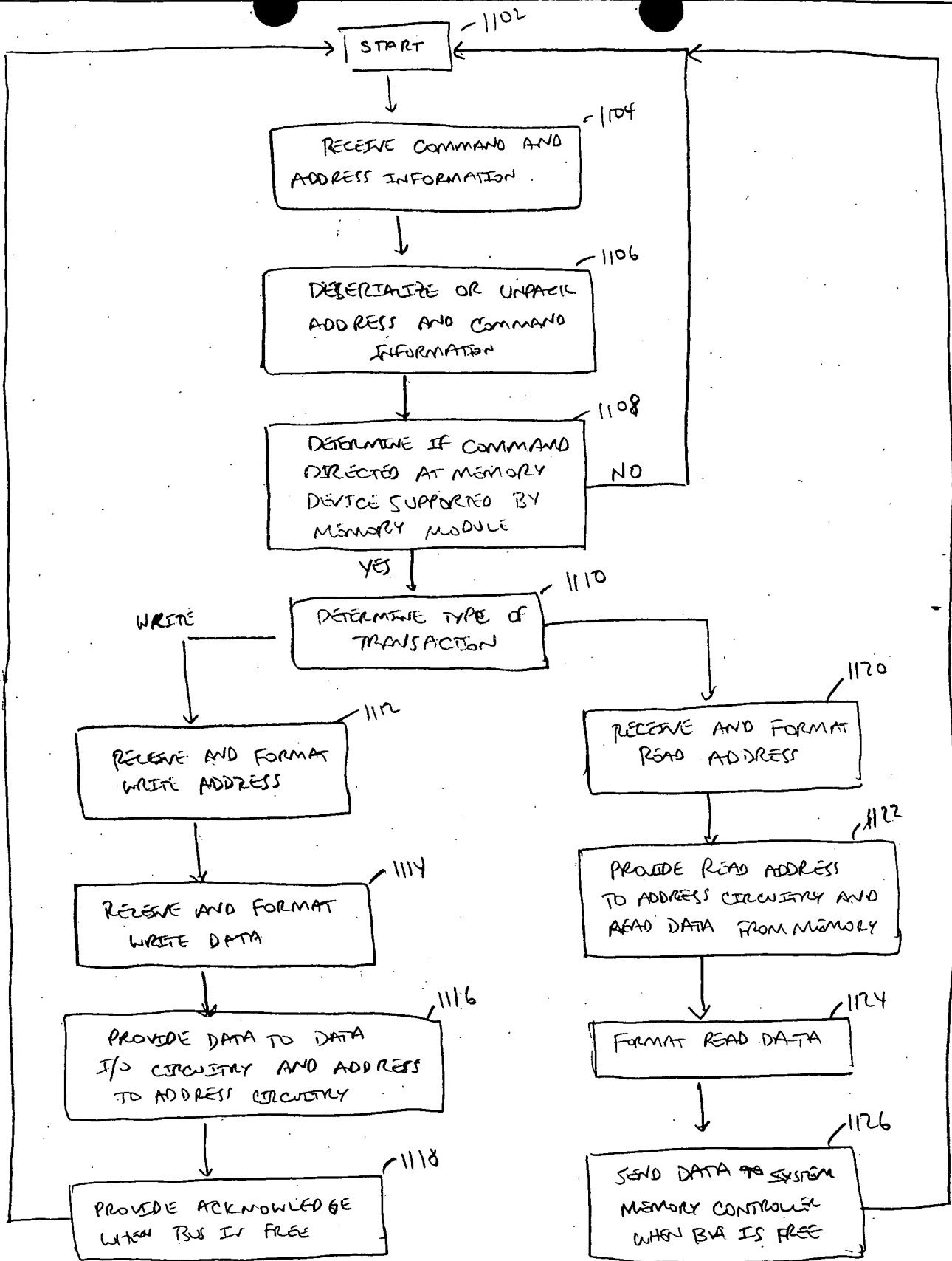
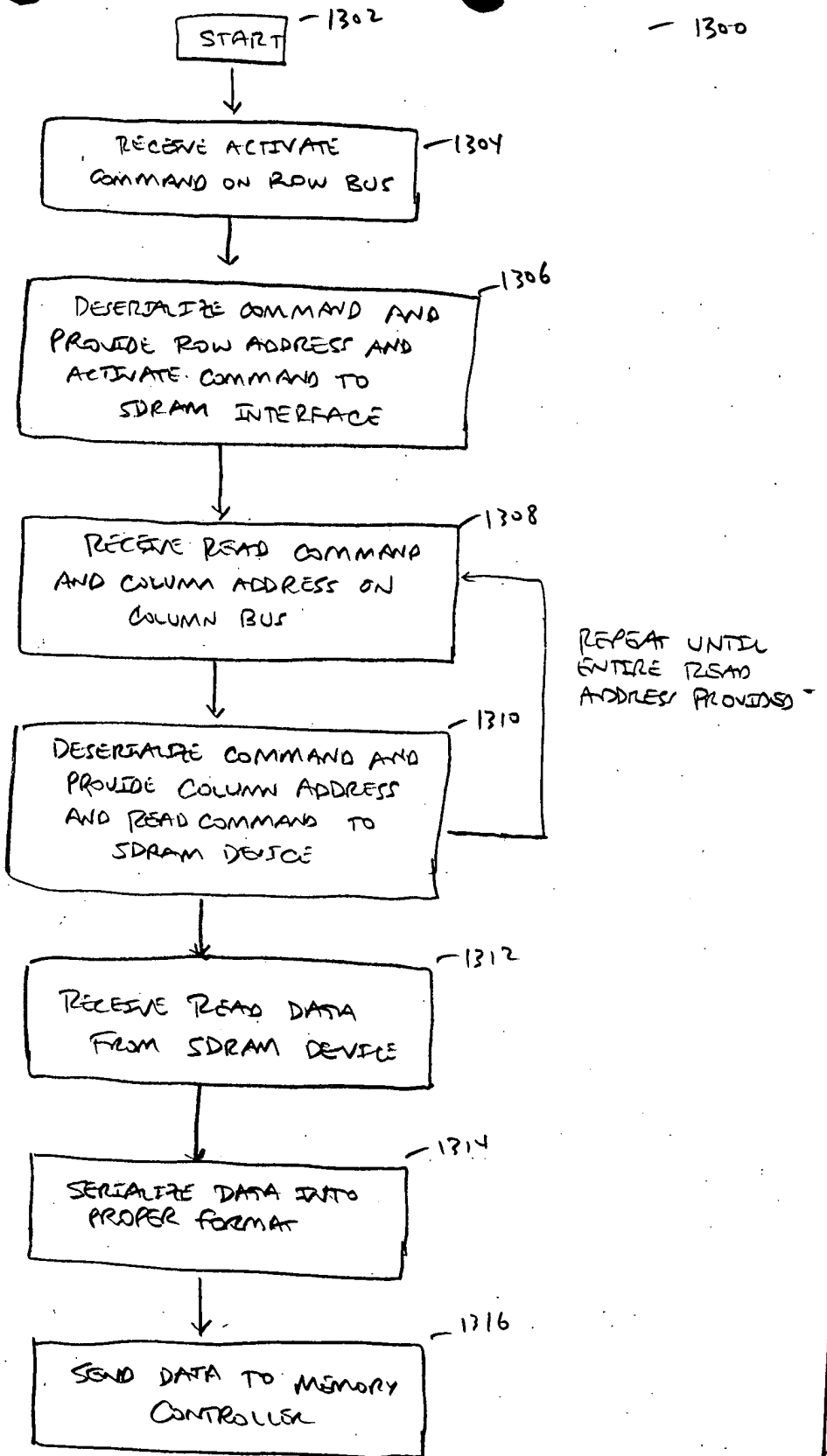


FIG. 11





CLK 1228

Row 1234

Column 1236

DATA 1232

CLK 1208, 1216

ADDR 1214, 1222

Control Bus 1238, 1246

DATA 1224-1227

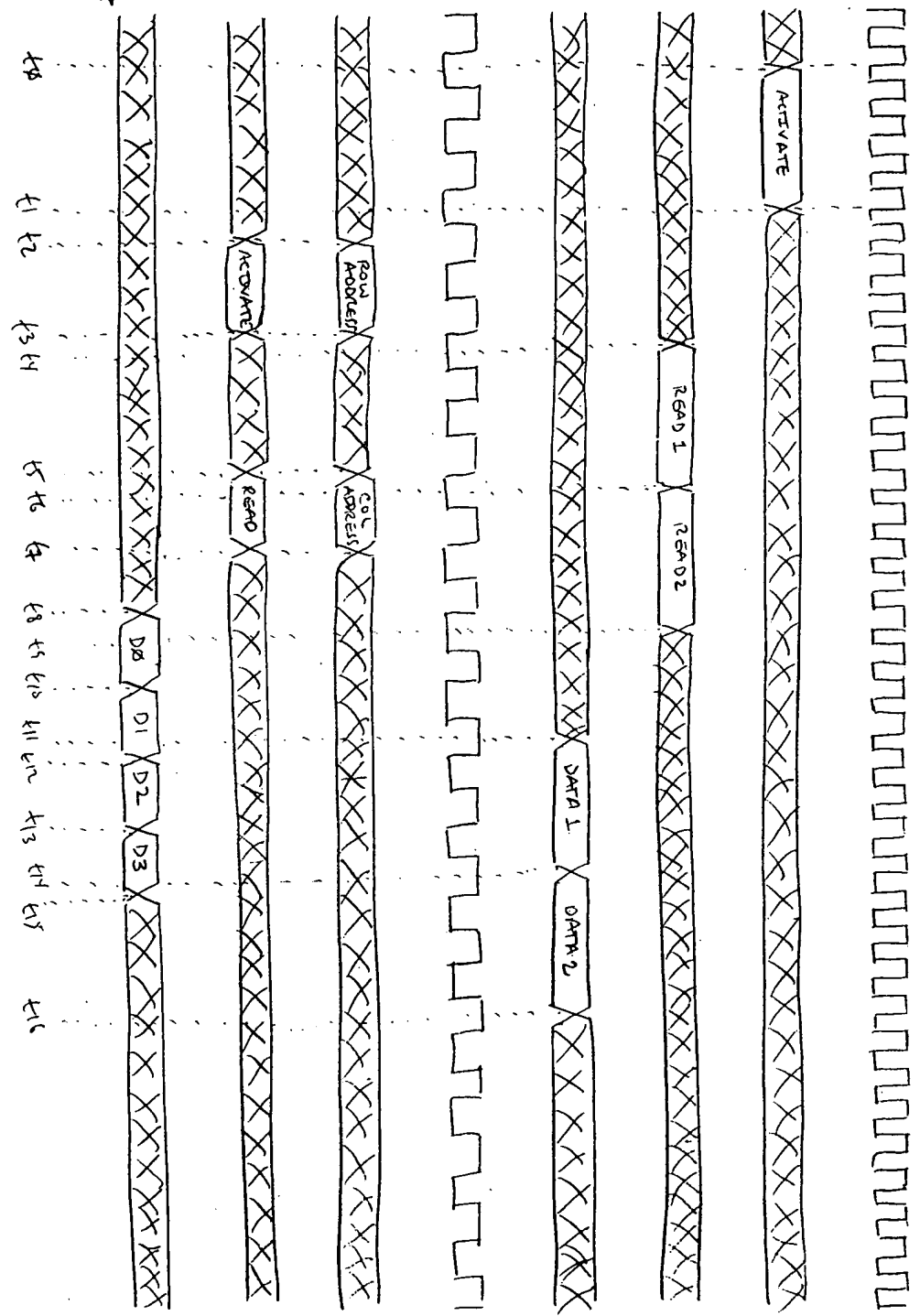


Fig. 14

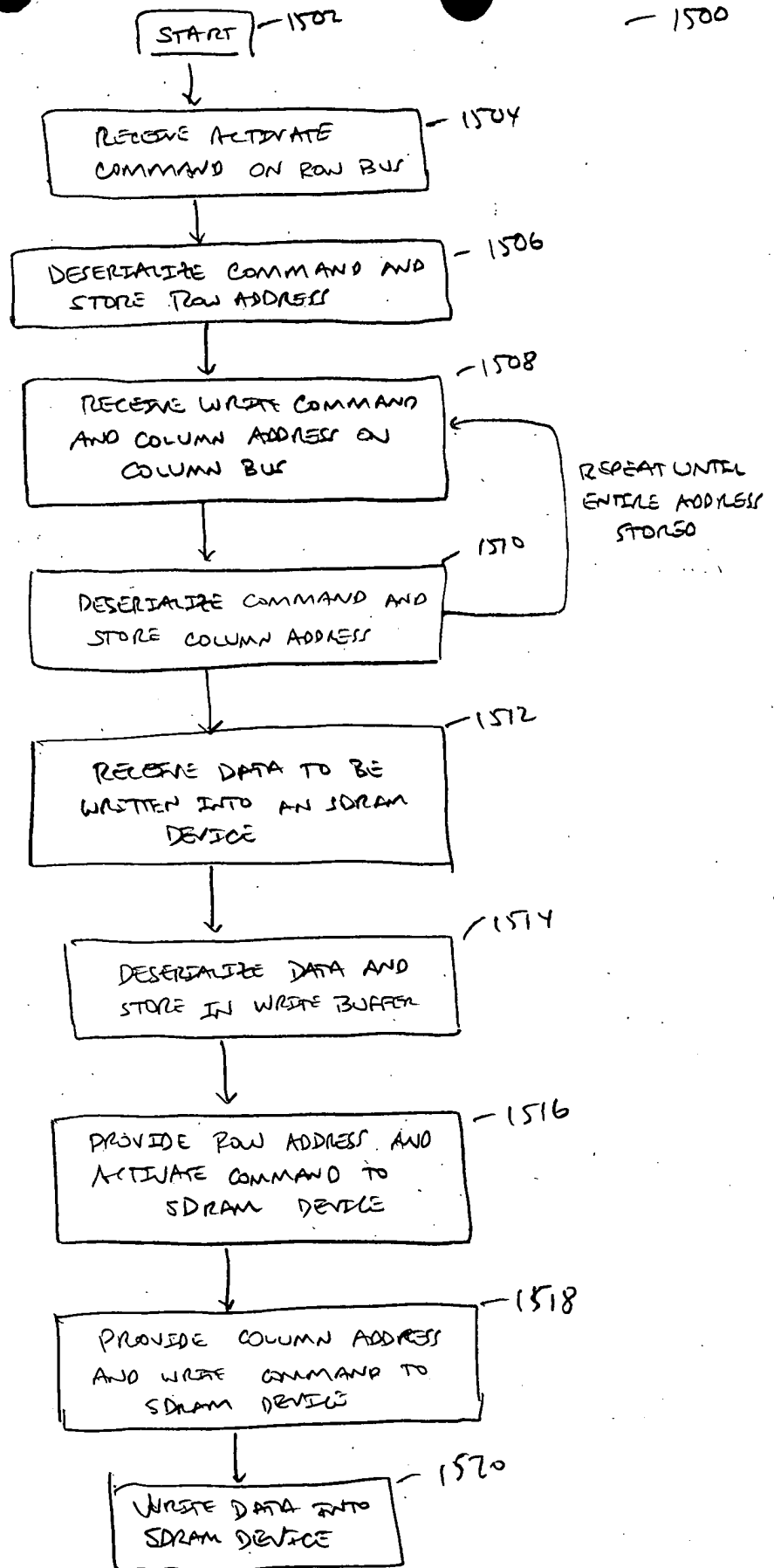
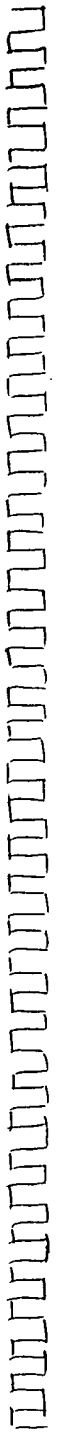


FIG. 15

CLK 1228



Row 1234



Column 1236



DATA 1232



CLK 1208, 1216



ADDR 1214, 1222



Control BUS 1238, 1240



DATA 1224-1227

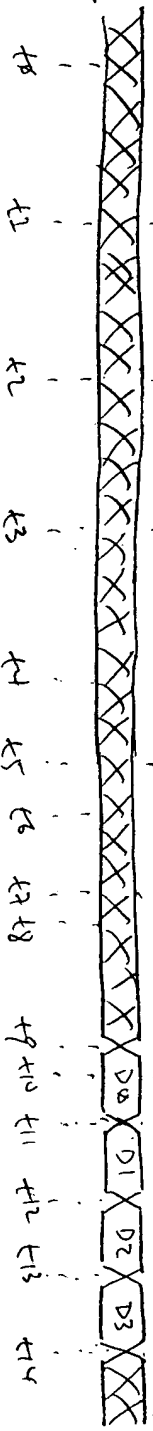
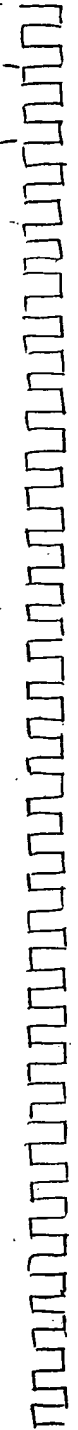


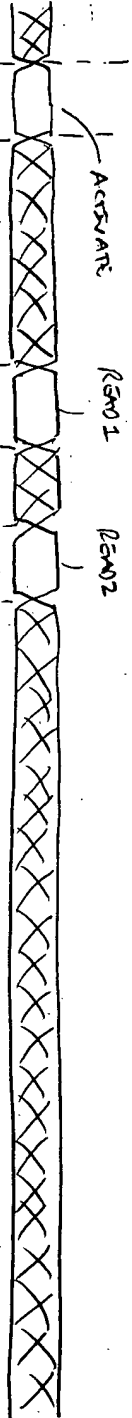
FIG. 16



Clock 1228



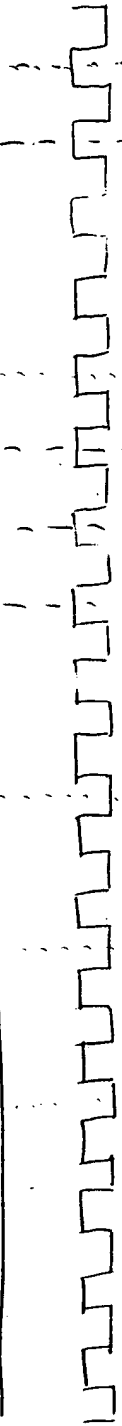
CONTROL BUS 1235



DATA 1232



Clock 1208, 1216



ADDR 1214, 1222



CONTROL BUS 1238, 1240



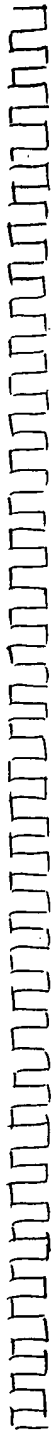
DATA 1214-1217



t0 t1 t2 t3 t4 t5 t6 t7 t8 t9 t10 t11 t12 t13 t14 t15 t16 t17

Fig. 17

CLK 1228

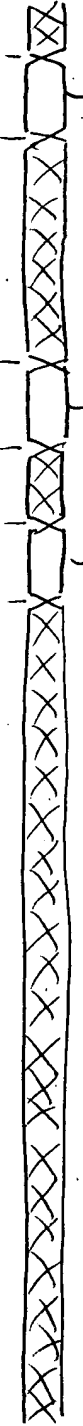


ACTIVE

WRITE 1

WRITE 2

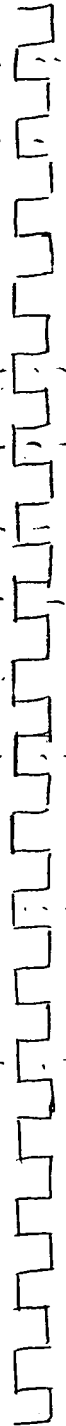
CONTROL BUS 1235



DATA 1232



CLK 1208, 1216



ADDR 1214, 1222



CONTROL BUS 1238, 1240



DATA 1224-1237

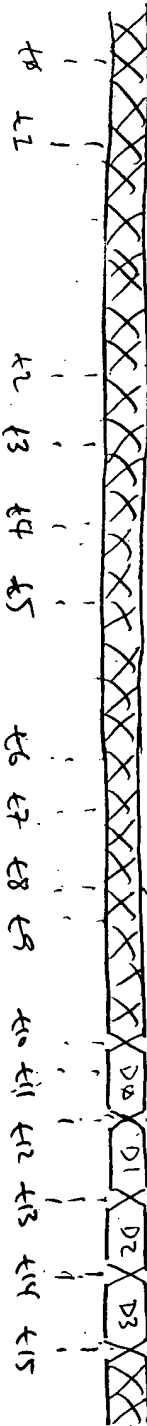


FIG. 18

